



Signal Integrity Effects in Custom IC and ASIC Designs

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"...offers a tutorial guide to IC designers who want to move to the next level of chip design by unlocking the secrets of signal integrity."

—**Jake Burma**, Senior Vice President, Worldwide Research & Development, Cadence Design Systems, Inc.

- Covers signal integrity effects in high performance Radio Frequency (RF) IC
- Brings together research papers from the past few years that address the broad range of issues faced by IC designers and CAD managers now and in the future

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